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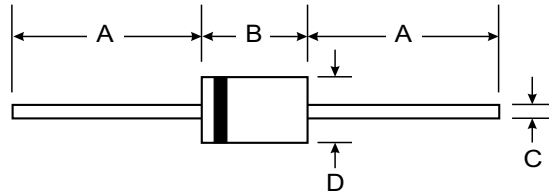
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Jameco Part Number 177949

Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Application
- Plastic Material: UL Flammability Classification Rating 94V-0



DO-41 Plastic		
Dim	Min	Max
A	25.40	—
B	4.06	5.21
C	0.71	0.864
D	2.00	2.72
All Dimensions in mm		

Mechanical Data

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 0.3 grams (approx)
- Mounting Position: Any
- Marking: Type Number

Maximum Ratings and Electrical Characteristics @ T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	1N5817	1N5818	1N5819	Unit
Peak Repetitive Reverse Voltage	V _{RRM}				V
Working Peak Reverse Voltage	V _{RWM}	20	30	40	V
DC Blocking Voltage	V _R				V
RMS Reverse Voltage	V _{R(RMS)}	14	21	28	V
Average Rectified Output Current (Note 1)	I _O	1.0			A
@ T _L = 90°C					
Non-Repetitive Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	25			A
Forward Voltage	V _{FM}	0.450	0.550	0.60	V
@ I _F = 1.0A		0.750	0.875	0.90	
@ I _F = 3.0A					
Peak Reverse Leakage Current	I _{RM}	1.0			mA
@ T _A = 25°C					
@ T _A = 100°C		10			
Typical Junction Capacitance (Note 2)	C _j	110			pF
Typical Thermal Resistance Junction to Lead (Note 1)	R _{θJL}	60			K/W
Operating and Storage Temperature Range	T _j , T _{STG}	-65 to +125			°C

- Notes:
1. Valid provided that leads are kept at ambient temperature at a distance of 9.5mm from the case.
 2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

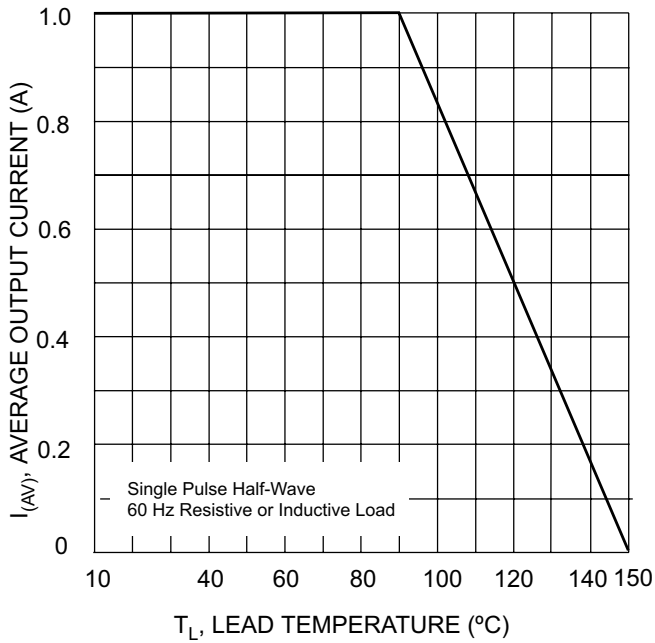


Fig. 1 Forward Current Derating Curve

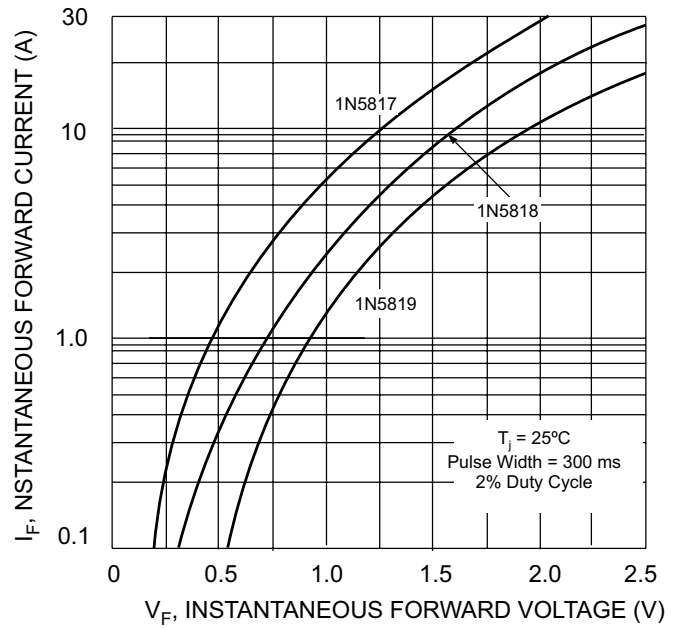


Fig. 2 Typical Forward Characteristics

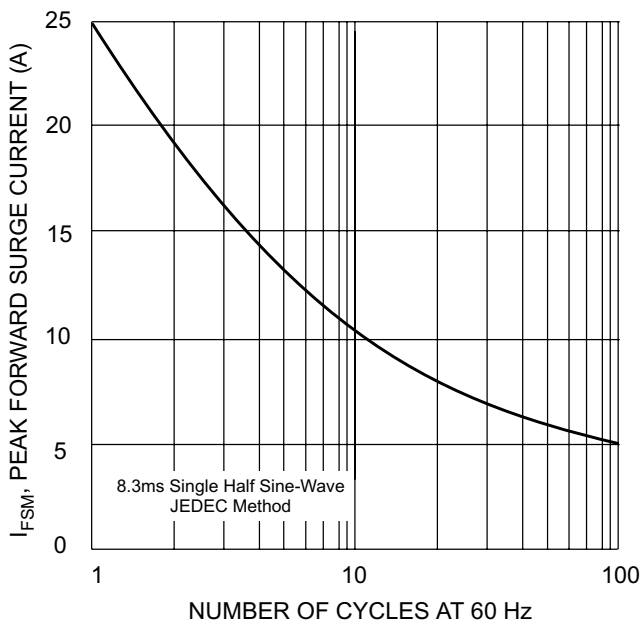


Fig. 3 Maximum Non-Repetitive Peak Fwd Surge Current

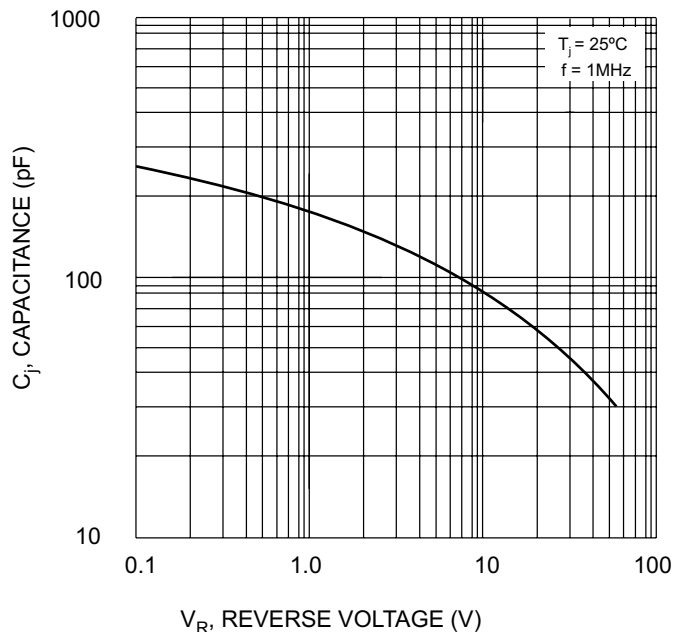


Fig. 4 Typical Junction Capacitance