

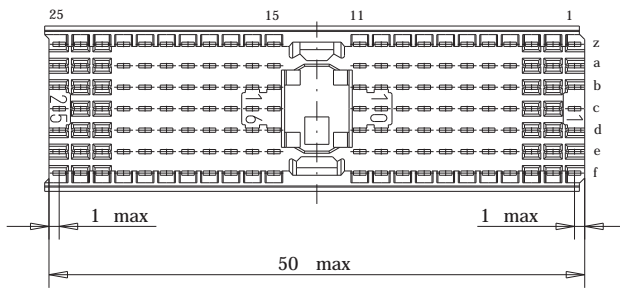
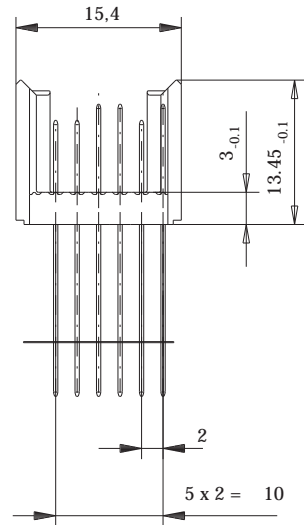
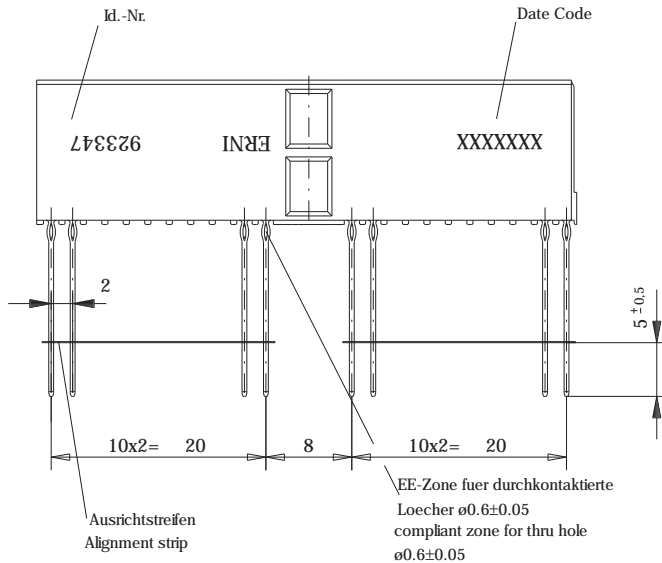
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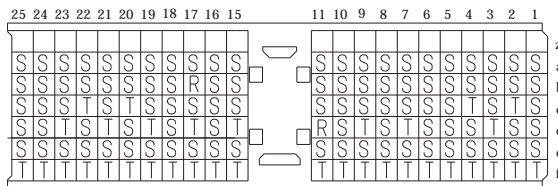
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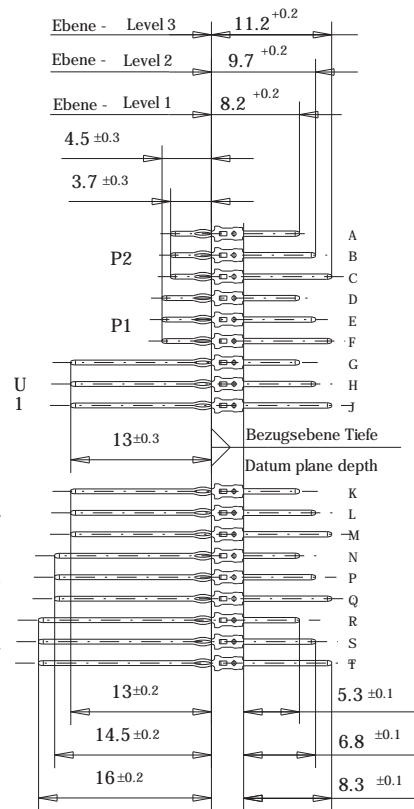
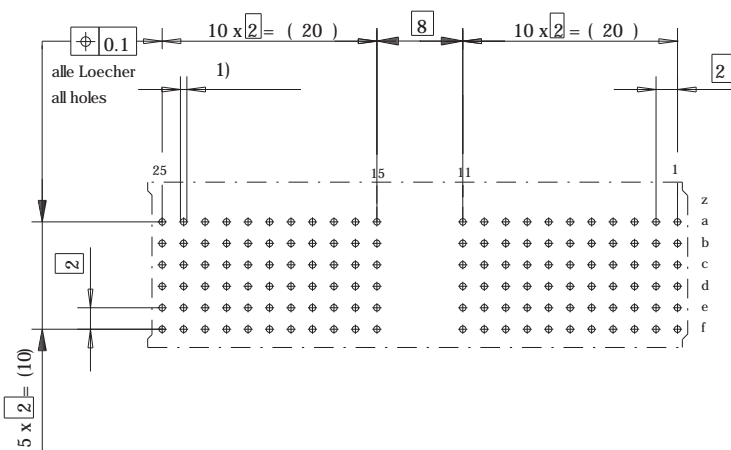


Bestueckungsplan - contact layout



Lochbild fuer Leiterplatte (Bestueckungsseite)

Board hole pattern (Component mounting side)



- 1) $\phi 0,6 \pm 0,05$ Durchmesser des metallisierten Loches
- $\phi 0,6 \pm 0,05$ Diameter of finished plated-through hole
- $\phi 0,7 \pm 0,02$ Bohrungsdurchmesser des Loches
- $\phi 0,7 \pm 0,02$ Diameter of drilled hole

Schichtaufbau im metallisierten Loch siehe Zeichnung 114406
Metal plating of plated-through hole see drawing 114406

Fehlende Maße und Angaben nach IEC 61076-4-101
Missing information and dimensions per 61076-4-101

Information: R1 Level 1,2,3 siehe Bestueckungsplan/ see contact layout ohne Fixierstift / without locating peg	Tolerances	 All Dimensions in mm	Scale	2:1
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