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# Quality Engineering Test Report

**SERIES: SLW05B 5W DC-DC SINGLE OUTPUT CONVERTER**

**SAMPLE:**

|             |              |
|-------------|--------------|
| A:SLW05B-05 | +5V / 1000mA |
| B:SLW05B-09 | +9V / 556mA  |
| C:SLW05B-12 | +12V / 417mA |
| D:SLW05B-15 | +15V / 333mA |

| NO         | TEST ITEM                                                        | TEST CONDITION / SPECIFICATION                                                                                                                                     | RESULT                                                                                   | VERDICT  |
|------------|------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------|----------|
| 1          | DC INPUT VOLTAGE RANGE                                           | I/P: TESTING<br>O/P: FULL LOAD                                                                                                                                     | SPEC: 18~36VDC<br><br>A:11~36VDC                                                         | P        |
| 2          | LINE REGULATION                                                  | I/P: 18~36VDC<br>O/P: FULL LOAD                                                                                                                                    | SPEC: ±0.2% (MAX.)<br><br>A:-0%~+0%<br>B:-0%~+0%<br>C:-0%~+0%<br>D:-0%~+0%               | P        |
| 3          | LOAD REGULATION                                                  | I/P: 24VDC<br>O/P: MIN. TO FULL LOAD                                                                                                                               | SPEC: ±1%<br><br>A:-0%~+0%<br>B:-0.1%~+0%<br>C:-0.1%~+0%<br>D:-0.1%~+0%                  | P        |
| 4          | OUTPUT VOLTAGE TOLERANCE                                         | I/P: 18~36VDC<br>O/P: MIN. TO FULL LOAD                                                                                                                            | SPEC: NONE<br><br>A:-0.1%~+0%<br>B:-0.1%~+0%<br>C:-0.1%~+0%<br>D:-0.1%~+0%               | P        |
| 5          | RIPPLE&NOISE                                                     | I/P: 24VDC<br>O/P: FULL LOAD                                                                                                                                       | SPEC: 100mV<br><br>A:20mV<br>B:20mV<br>C:22mV<br>D:12mV                                  | P        |
| 6          | DC INPUT CURRENT                                                 | I/P: 24VDC<br>O/P: FULL LOAD                                                                                                                                       | SPEC: 0.27A<br><br>A:0.26A                                                               | P        |
| 7          | OVER LOAD PROTECTION                                             | I/P: 24VDC<br>O/P: TESTING                                                                                                                                         | SPEC: NONE<br><br>A:183%<br>B:171%<br>C:246%<br>D:212%                                   | P        |
| 8          | O/P VOLTAGE ACCURACY                                             | I/P: 24VDC<br>O/P: MIN. LOAD                                                                                                                                       | SPEC: ±1% (MAX.)<br><br>A:+0.32%<br>B:+0.67%<br>C:+0.91%<br>D:-0.40%                     | P        |
| 9          | SET UP TIME                                                      | I/P: 24VDC<br>O/P: FULL LOAD                                                                                                                                       | SPEC: NONE<br><br>A:6.2mS                                                                | P        |
| 10         | EFFICIENCY                                                       | I/P: 24VDC<br>O/P: FULL LOAD                                                                                                                                       | SPEC: 73%<br><br>A:80.0%<br>B:82.6%<br>C:82.6%<br>D:82.1%                                | P        |
| 11         | INSULATION RESISTANCE                                            | SPEC: I/P-O/P 500VDC/100M Ohms MIN.                                                                                                                                | A:I/P-O/P >100M Ohms                                                                     | P        |
| 12         | DIELECTRIC / WITHSTAND VOLTAGE                                   | SPEC: I/P-O/P: 1000VDC/1 min<br>(10mA CUT-OFF)                                                                                                                     | A: NO BREAK<br>I/P-O/P :<0.02mA                                                          | P        |
| 13         | BURN-IN TEST                                                     | I/P: 24VDC<br>TA: 25°C<br>O/P: FULL LOAD<br>BURN-IN DURATION: 2 hrs                                                                                                | A: NO BREAK<br>B: NO BREAK<br>C: NO BREAK<br>D: NO BREAK                                 | P        |
| 14         | TEMPERATURE RISE TEST T <sub>rise</sub> OF PARTS                 | A: I/P : 24VDC<br>O/P : FULL LOAD<br>AFTER 4 hrs BURN-IN<br>TA: 25°C                                                                                               | POSITION<br>CASE<br>SAMPLE<br>SLW05B-05<br>TEMP<br>42.0°C<br>T <sub>rise</sub><br>17.0°C | P        |
| 15         | CONSTRUCTION INSPECTION<br>(FOR QC INSPECTION<br>REFERENCE ONLY) | A:<br>1. PACKING : 上,下加泡棉再外加紙盒包裝<br>2. MARKING : MODEL<br>3. MECHANICAL : FIVE-SIDED SHIELD METAL CASE<br>4. DIMENSIONS : LXWXH=50.8X25.4X11.27mm, PIN: 1.0Øx8.3mm |                                                                                          |          |
| DATE       | SAMPLE                                                           | TEST RESULT                                                                                                                                                        | TEST                                                                                     | APPROVAL |
| 1998.11.19 | SLW05B-05<br>SLW05B-09<br>SLW05B-12<br>SLW05B-15                 | PASS                                                                                                                                                               | T.K.CHENG                                                                                | MAX LIN  |