

Proven architecture based on ten years' experience in ARM-based standard products - all based on the industry-standard ARM 32-bit RISC processor architecture

[Products](#) > > [AT91SAM 32-bit ARM-based Microcontrollers](#) > Tool Card

AT91SAM 32-bit ARM-based

- [Home](#)
- [Overview](#)
- [Devices](#)
- [Tools & Software](#)
- [Datasheets](#)
- [Application Notes](#)
- [Other Documents](#)
- [Frequently Asked Questions](#)
- [MCU Support Center](#)
- [Third Party Support](#)
- [Request Samples](#)
- [What's Changed](#)

AT91SAM-ICE JTAG Emulator

Description:

SAM-ICE is a JTAG emulator designed for all Atmel AT91 ARM7/ARM9 cores. DLL files to support the SAM-ICE are available on the www.segger.com web site in the "Downloads", then "J-Link ARM" sub-areas.

- Any ARM7/ARM9 Atmel core supported, including Thumb™ mode
- J-Link compatible mode
- RDI compliant
- Download speed up to 720KByte/se
- No power supply required (powered through USB port)
- Maximum JTAG speed up to 12 MHZ
- Auto speed recognition
- All JTAG signals can be monitored, target voltage can be measured
- Fully plug and play compatible
- USB and 20-pin flat cable included
- J-Link server (connects to SAM-ICE via TCP/IP) included.
- GDB Server included



Documents:

AT91SAM-ICE User Guide (User Guide, 16 pages, revision B, updated 3/08)

Related Devices:

[AT91RM9200](#) [AT91SAM7A3](#) [AT91SAM7L128](#) [AT91SAM7L64](#) [AT91SAM7S128](#) [AT91SAM7S256](#)
[AT91SAM7S32](#) [AT91SAM7S321](#) [AT91SAM7S512](#) [AT91SAM7S64](#) [AT91SAM7SE256](#) [AT91SAM7SE32](#)
[AT91SAM7SE512](#) [AT91SAM7X128](#) [AT91SAM7X256](#) [AT91SAM7X512](#) [AT91SAM7XC128](#)
[AT91SAM7XC256](#) [AT91SAM7XC512](#) [AT91SAM9260](#) [AT91SAM9261](#) [AT91SAM9261S](#) [AT91SAM9263](#)
[AT91SAM9R64](#) [AT91SAM9RL64](#) [AT91SAM9XE128](#) [AT91SAM9XE256](#) [AT91SAM9XE512](#)